



Pin No.	Symbol	Description
1	FG	Frame Ground.
2	VSS	GND.
3	VDD	Power supply for logic and LCD.
4	VO	Operating voltage for LCD.
5	/RR	Command and data write signal.
6	/RD	Data and status read signal.
7	/CE	Chip enable signal.
8	C/D	Command / Data selection.
9	NC	NC
10	/RST	Reset signal (Low effective).
11-18	DBO-DIR†	Data bus.
19	FS	Selection of font H: 6 X 8 L: 8 X 8.
20	Vout	Negative voltage output to LCD.
21	LED A	Backlight anode (+5V).
22	LED K	Backlight cathode (+0V).
23	LED K	Backlight cathode (+0V).
24	LED A	Backlight anode (+5V).
25	Vout	Negative voltage output to LCD.
26	VO	Operating voltage for LCD.
27	VSS	GND.
28	VDD	Power supply for logic and LCD.
29	NC	NC
30	HSCP	Shift clock pulse for column driver of upper area of LCD.
31	LP	Latch pulse for column driver shift clock pulse for row driver.
32	FR	Frame Signal.
33	DATA	Synchronous signal for row driver.
34	ED	Serial=H:Data output for even columns in both upper and lower areas of LCD Serial=L:Data output for columns in both upper and lower areas of LCD

ABSOLUTE MAXIMUM RATING						
Item	Symbol	Min	Type	Max	Unit	
Supply Voltage	Log e VDD	4.8		5.0	V	
LCD Operating Voltage	VDD-V0	-		18, 15, 19, 0	V	
LED Voltage	Vled	4.8		5.0	V	
LED Current (Side LED)	ILED	-		190	200	mA

Description	
1.	DISPLAY TYPE: SW Negative Blue* (SW / Positive, Yellow-Green).
2.	Viewing Direction of CLOCK : (12 o'clock).
3.	Polarizer Mode: Transmissive; (Transflective ; Reflective).
4.	Color of Backlight: White ; (Yellow/Green).
5.	Operating voltage option : 5V .
6.	Operating Temp: -20° ~ 70° C.
7.	Storage Temp: -30° ~ 80° C.

REVISION RECORD	DATE	DESCRIPTION	PROJ(3)
1			
2			
3			
4			
5			
6			

Model No. :	ET-Q240128AV3	PROJ(3):	A
Part No.:	SPEC-ET-G240128AV3	VER.:	
DRAWN	OY	DATE	2011.06.09
CHECKED		DATE	
APPROVED		DATE	

SHENZHEN ET TECHNOLOGY CO.,LTD.		E-mail: ees@easet.com	
TITLE: LCM OUTLINE DIMENSION		GEN.TOL.: ±0.2	

1	2	3	4	5	6	7	8
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