



Pin. No	Symbol	Description
1	FG	Frame Ground.
2	VSS	GND.
3	VDD	Power supply for logic and LCD.
4	V0	Operating voltage for LCD.
5	/WR	Command and data write signal.
6	/RD	Data and status read signal.
7	/CE	Chip enable signal.
8	C/D	Command / Data selection.
9	NC	
10	/RST	Reset signal (Low effective).
11-18	DB0-DRT	Data bus.
19	FS	Selection of font H: 6 X 8 L: 8 X 8.
20	Vout	Negative voltage output to LCD
21	LED A	Backlight anode (+5V).
22	LED K	Backlight cathode (+0V).
23	LED K	Backlight cathode (+0V).
24	LED A	Backlight anode (+5V).
25	Vout	Negative voltage output to LCD
26	V0	Operating voltage for LCD.
27	VSS	GND.
28	VDD	Power supply for logic and LCD.
29	NC	
30	HSCP	Shift clock pulse for column driver of upper area of LCD
31	LP	Latch pulse for column driver shift clock pulse for row driver
32	FR	Frame Signal
33	DATA	Synchronous signal for row driver
34	ED	Ssel:=H:Data output for even columns in both upper and lower areas of LCD Ssel:=L:Data output for columns in both upper and lower areas of LCD

ABSOLUTE MAXIMUM RATING				
Item	Symbol	Min	Typ	Max
Supply voltage	Logic VDD	4.8	5.0	5.2
LCD Operating Voltage	VDD-V0	-	18.15	19.0
LED Voltage	Vled	4.8	5.0	5.2
LED Current (Side LED)	Iled	-	190	200
				mA

Description	
1.	DISPLAY TYPE: STN / Negative Blue: (STN / Positive, Yellow-Green).
2.	Viewing Direction of CLOCK: (12 O'CLOCK).
3.	Polarizer Mode: Transmissive: (Transflective: Reflective).
4.	Color of Backlight: White: (Yellow/Green).
5.	Operating voltage option: .5V.
6.	Operating Temp: -20° ~ 70° C.
7.	Storage Temp: -30° ~ 80° C.

REVISION RECORD		DATE	SHENZHEN ET TECHNOLOGY CO.,LTD.
1			Model No.: ET-G240128AV1
2			Part No.: SPEC-ET-G240128AV1
3			VER.: A
4	DRAWN	OY	DATE: 2011.06.09
5	CHECKED		DATE:
6	APPROVED		DATE:

TITLE: LCM OUTLINE DIMENSION		GEN.TOL.: ±0.2
PROJ.3:		
SHEET:	1/1	
UNIT:	mm	
SCALE:	1:1	